

0172 JUN 07 2004
PATENT & TRADEMARK OFFICE

FIG. 1
LOGIC CONES

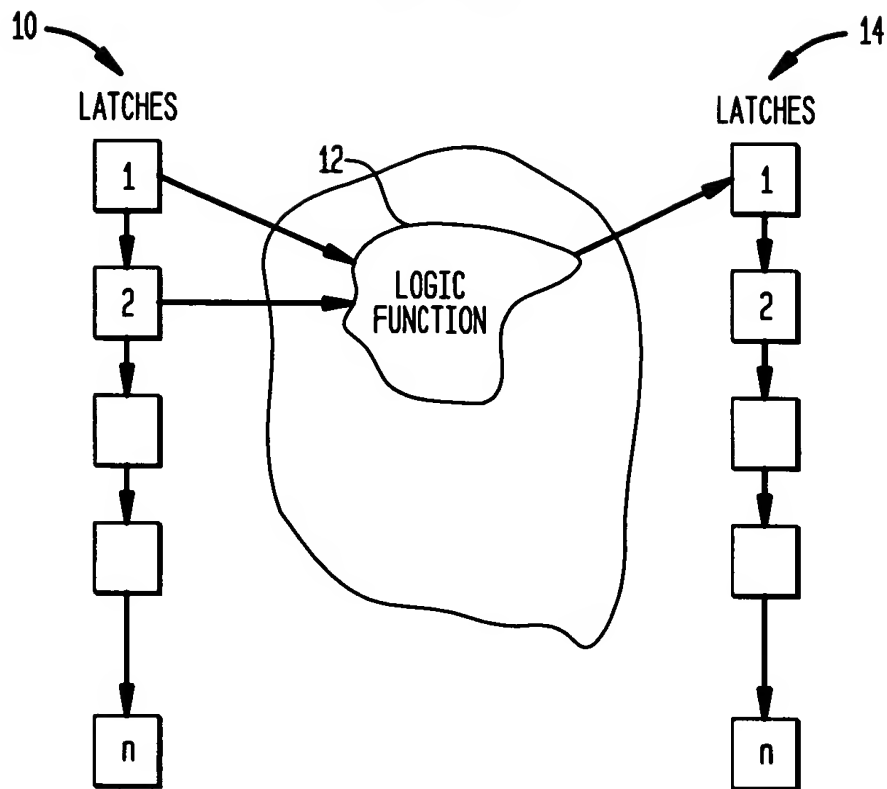


FIG. 2
STRUCTURE

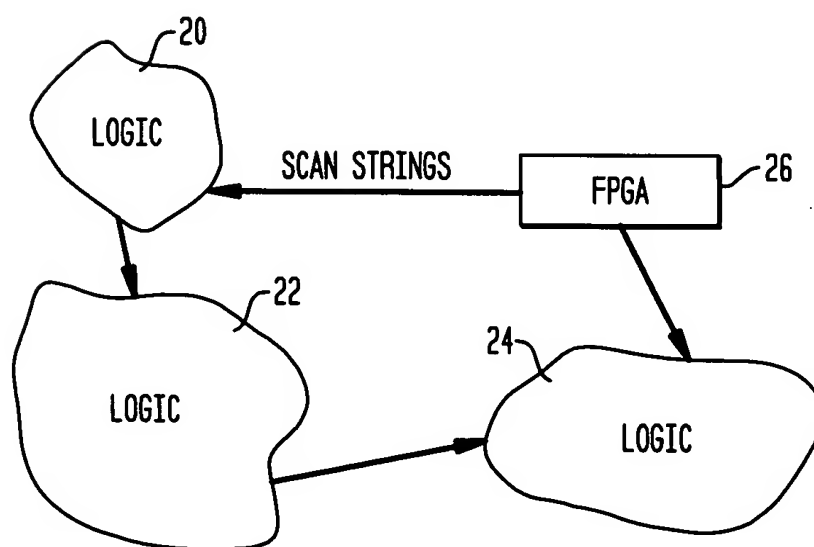


FIG. 3
FUNCTION TO FPGA

